

Serial No. 10/782,784

Docket No. P-0646

Reply to Office Action dated April 5, 2005

AMENDMENTS TO THE SPECIFICATION:

Please replace paragraph [40] with the following amended paragraph:

[0040] If loading a control voltage from the memory 80 is completed, the CPU 100 may disable the signal converter [[90]] 70 so as not to disturb a phase lock looping.